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S1	2	"6745384".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/01 14:39
S2	2	"6009272".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/01 15:32
S4	0	for with "&&"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/04 12:25
S5	5214	"&&"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/04 12:25
S6	0	"&&" same for	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/04 12:29
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S10	14	S5 and for	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/04 12:31
S11	25	loop with "&&"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/04 12:31



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Relevance scale ☐ ☐ ☐ ☐ ☐**1 A study on the implementation of three-valued logic**

H. T. Mouftah

May 1976 **Proceedings of the sixth international symposium on Multiple-valued logic**Full text available: pdf(226.94 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A system of three-valued algebra, based on operators previously defined, is presented. COS/MOS integrated circuits are applied to realize the three-valued operators. An approach to avoid the use of diodes in COS/MOS integrated circuit technology is given and a method for reduction of logical circuitry is discussed.

**2 Burroughs Truth Function Evaluator**

William Miehle

April 1957 **Journal of the ACM (JACM)**, Volume 4 Issue 2Full text available: pdf(214.88 KB) Additional Information: [full citation](#), [references](#), [index terms](#)

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